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APPLICANTS

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\*\* CONTINUING DATA \*\*\*\*\*

*PEP* This application is a DIV of 10/156,955 05/30/2002  
 PAT 6,664,624

\*\* FOREIGN APPLICATIONS \*\*\*\*\*

JAPAN 2001-165701 05/31/2001

*PEP*

IF REQUIRED, FOREIGN FILING LICENSE GRANTED

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Foreign Priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no	STATE OR	SHEETS	TOTAL	INDEPENDENT
35 USC 119 (a-d) conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance	COUNTRY	DRAWING	CLAIMS	CLAIMS
Verified and Acknowledged	JAPAN	6	3	1

Examiner's Signature *[Signature]* Initials *PEP*

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TITLE

Semiconductor device and manufacturing method thereof

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